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(54) PLASMA CURING OF PECVD HMDSO FILM FOR OLED APPLICATIONS

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- (52) U.S. Cl. CPC H01L 51/5256 (2013.01); C23C 16/401 (2013.01); C23C 16/56 (2013.01)
- Field of Classification Search CPC H01L 21/00 See application file for complete search history.

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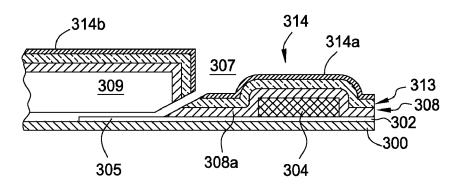
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ABSTRACT

Methods for forming an OLED device are described. An encapsulation layer having a buffer layer sandwiched between barrier layers is deposited over an OLED structure. The buffer layer is deposited on the first barrier layer and is cured with a fluorine-containing plasma at a temperature less than 100 degrees Celsius. The second barrier layer is then deposited on the buffer layer.

20 Claims, 3 Drawing Sheets



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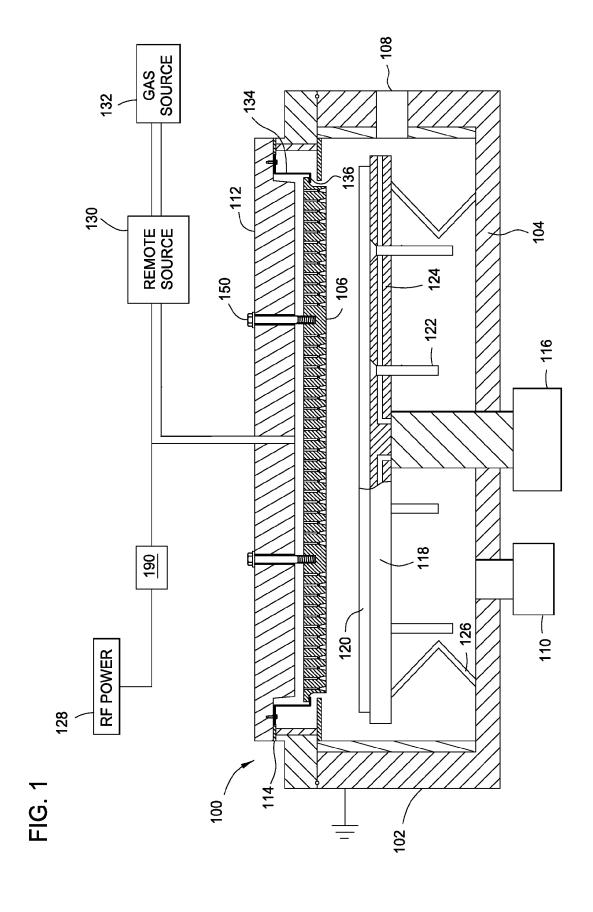
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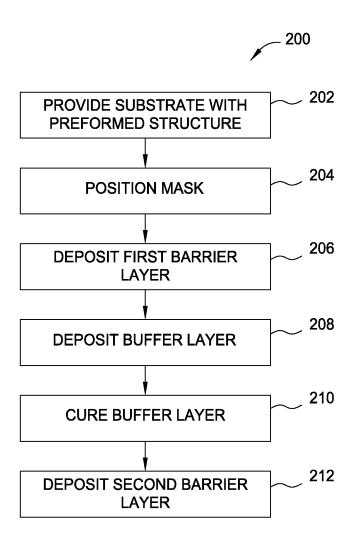
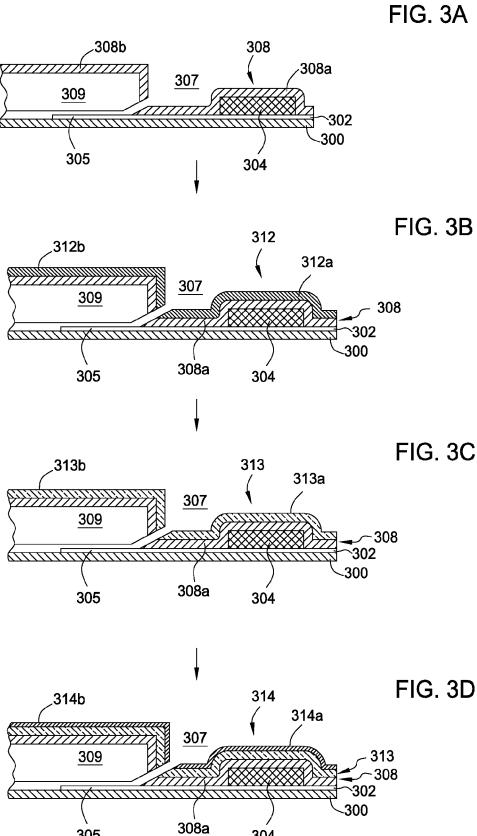


FIG. 2



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PLASMA CURING OF PECVD HMDSO FILM FOR OLED APPLICATIONS

CROSS-REFERENCE TO RELATED APPLICATIONS

This application claims benefit of U.S. Provisional Patent Application Ser. No. 61/775,961 (APPM 20432L), filed Mar. 11, 2013, which is herein incorporated by reference.

BACKGROUND

1. Field

Embodiments of the invention generally relate to methods for forming an organic light emitting diode (OLED) device, more particularly, to methods for encapsulating an OLED 15 structure.

2. Description of the Related Art

An OLED is used in the manufacture of television screens, computer monitors, mobile phones, other hand-held devices, etc. for displaying information. OLED displays 20 have gained significant interest recently in display applications due to their faster response time, larger viewing angles, higher contrast, lighter weight, low power and amenability to flexible substrates such as compared to liquid crystal displays (LCD).

OLED structures may have a limited lifetime, characterized by a decrease in electroluminescence efficiency and an increase in drive voltage. A main reason for the degradation of OLED structures is the formation of non-emissive dark spots due to moisture or oxygen ingress. For this reason, OLED structures are typically encapsulated by an organic layer sandwiched between inorganic layers. The organic layer is utilized to fill any voids or defects in the first inorganic layer such that the second inorganic layer has a substantially uniform surface or deposition.

Therefore, an improved method and apparatus for encapsulating an OLED structure is needed.

SUMMARY

Methods for forming an OLED device are described. An encapsulation layer having a buffer layer sandwiched between barrier layers is deposited over an OLED structure. The buffer layer is deposited on the first barrier layer and is cured with a fluorinated plasma at a temperature less than 45 100 degrees Celsius. The second barrier layer is then deposited on the buffer layer.

In one embodiment, a method for forming an OLED device is disclosed. The method includes depositing a first barrier layer on a region of a substrate having an OLED 50 structure disposed thereon, depositing a buffer layer on the first barrier layer, curing the buffer layer with a fluorine-containing plasma at a temperature less than about 100 degrees Celsius, and depositing a second barrier layer on the cured buffer layer.

In another embodiment, a method for forming an OLED device is disclosed. The OLED device includes depositing a first barrier layer on a region of a substrate having an OLED structure disposed thereon, depositing a buffer layer on the first barrier layer, curing the buffer layer with a fluorine-containing plasma for about 2 minutes, and depositing a second barrier layer on the cured buffer layer.

BRIEF DESCRIPTION OF THE DRAWINGS

So that the manner in which the above recited features of the present disclosure can be understood in detail, a more 2

particular description of the disclosure, briefly summarized above, may be had by reference to embodiments, some of which are illustrated in the appended drawings. It is to be noted, however, that the appended drawings illustrate only typical embodiments of this disclosure and are therefore not to be considered limiting of its scope, for the disclosure may admit to other equally effective embodiments.

FIG. 1 is a schematic, cross-sectional view of a PECVD apparatus that may be used to perform the methods described herein.

FIG. 2 is a flow diagram of a method for forming an OLED device in accordance with one embodiment of the present invention.

FIGS. 3A-3D illustrate schematic cross-sectional views of the OLED device during different stages of the method of FIG. 2.

To facilitate understanding, identical reference numerals have been used, where possible, to designate identical elements that are common to the figures. It is contemplated that elements and features of one embodiment may be beneficially incorporated in other embodiments without further recitation.

DETAILED DESCRIPTION

Methods for forming an OLED device are described. An encapsulation layer having a buffer layer sandwiched between barrier layers is deposited over an OLED structure. The buffer layer is deposited on the first barrier layer and is cured with a fluorine-containing plasma at a temperature less than 100 degrees Celsius. The second barrier layer is then deposited on the buffer layer.

FIG. 1 is a schematic, cross sectional view of a plasma enhanced chemical vapor deposition (PECVD) apparatus that may be used to perform the operations described herein. The apparatus includes a chamber 100 in which one or more films may be deposited onto a substrate 120. The chamber 100 generally includes walls 102, a bottom 104 and a showerhead 106 which define a process volume. A substrate support 118 is disposed within the process volume. The process volume is accessed through a slit valve opening 108 such that the substrate 120 may be transferred in and out of the chamber 100. The substrate support 118 may be coupled to an actuator 116 to raise and lower the substrate support 118. Lift pins 122 are moveably disposed through the substrate support 118 to move the substrate 120 to and from the substrate receiving surface. The substrate support 118 may also include heating and/or cooling elements 124 to maintain the substrate support 118 at a desired temperature. The substrate support 118 may also include RF return straps 126 to provide an RF return path at the periphery of the substrate support 118.

The showerhead **106** is coupled to a backing plate **112** by a fastening mechanism **150**. The showerhead **106** may be coupled to the backing plate **112** by one or more fastening mechanisms **150** to help prevent sag and/or control the straightness/curvature of the showerhead **106**.

A gas source 132 is coupled to the backing plate 112 to provide gas through gas passages in the showerhead 106 to a processing area between the showerhead 106 and the substrate 120. A vacuum pump 110 is coupled to the chamber 100 to maintain the process volume at a desired pressure. An RF source 128 is coupled through a match network 190 to the backing plate 112 and/or to the showerhead 106 to provide an RF current to the showerhead 106. The RF current creates an electric field between the showerhead 106 and the substrate support 118 so that a plasma

may be generated from the gases between the showerhead 106 and the substrate support 118.

A remote plasma source 130, such as an inductively coupled remote plasma source 130, may also be coupled between the gas source 132 and the backing plate 112. 5 Between processing substrates, a cleaning gas may be provided to the remote plasma source 130 so that a remote plasma is generated. The radicals from the remote plasma may be provided to chamber 100 to clean chamber 100 components. The cleaning gas may be further excited by the 10 RF source 128 provided to the showerhead 106.

The showerhead 106 may additionally be coupled to the backing plate 112 by showerhead suspension 134. In one embodiment, the showerhead suspension 134 is a flexible metal skirt. The showerhead suspension 134 may have a lip 136 upon which the showerhead 106 may rest. The backing plate 112 may rest on an upper surface of a ledge 114 coupled with the chamber walls 102 to seal the chamber 100.

FIG. 2 is a flow diagram of a method 200 for forming an OLED device according to various embodiments of the 20 invention. FIGS. 3A-3D illustrate schematic cross-sectional views of an OLED device during different stages of the method 200 of FIG. 2. The method 200 starts at process 202 by providing a substrate 300 having a preformed OLED structure 304 disposed thereon. The substrate 300 may have 25 a contact layer 302 disposed thereon, with the OLED structure 304 disposed on the contact layer 302, as shown in FIG. 3A.

At process 204, a mask 309 is aligned over the substrate 300 such that the OLED structure 304 is exposed through an 30 opening 307 unprotected by the mask 309, as shown in FIG. 3A. The mask 309 is positioned such that a portion 305 of the contact layer 302 adjacent the OLED structure 304 is covered by the mask 309 so that the any subsequently deposited material does not deposit on the portion 305. The 35 portion 305 of the contact layer 302 is the electrical contact for the OLED device, thus no material should be deposited thereon. The mask 309 may be made from a metal material, such as INVAR®.

At process 206, a first barrier layer 308 is deposited on the 40 substrate 100, as shown in FIG. 3A. The first barrier layer 308 has a first portion 308a and a second portion 308b. The first portion 308a of the first barrier layer 308 is deposited through the opening 307 onto a region of the substrate 300 exposed by the mask 309, which includes the OLED struc- 45 ture 304 and a portion of the contact layer 302. The second portion 308b of the first barrier layer 308 is deposited on the mask 309 covering a second region of the substrate 300, which includes the portion 305 of the contact layer 302. The first barrier layer 308 is a dielectric layer, such as silicon 50 nitride (SiN), silicon oxynitride (SiON), silicon dioxide (SiO₂), aluminum oxide (Al₂O₃), aluminum nitride (AlN), or other suitable dielectric layers. The first barrier layer 308 may be deposited by a suitable deposition technique, such as chemical vapor deposition (CVD), PECVD, physical vapor 55 deposition (PVD), spin-coating, or other suitable technique.

At process 208, after the first barrier layer 308 is deposited on the substrate 300, a buffer layer 312 is then deposited on the first barrier layer 308 on the substrate 300, as shown in FIG. 3B. A first portion 312a of the buffer layer 312 may 60 be deposited on the substrate 300 through the opening 307 of the mask 309 on the region of the substrate 300 exposed by the mask 309, covering the first portion 308a of the first barrier layer 308. A second portion 312b of the buffer layer 312 is deposited on the second portion 308b of the first barrier layer 308 disposed on the mask 309, which covers the portion 305 of the contact layer 302.

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The buffer layer 312 may be acrylate, methacrylate, acrylic acid, or the like. In one embodiment, the buffer layer 312 is plasma-polymerized hexamethyldisiloxane (pp-HMDSO). Deposition of the pp-HMDSO material layer is achieved by flowing an oxygen-containing gas and HMDSO gas. In one example, the oxygen-containing gas is nitrous oxide (N₂O). A low N₂O/HMDSO flow ratio (e.g., less than 2) may be maintained during processing to deposit a pp-HMDSO layer having characteristics including stress relief, particle conformality, and flexibility. These characteristics of the pp-HMDSO layer helps serving as a buffer layer to decouple the defects in the first barrier layer 308, as well as to planarize surface irregularity to form a smooth surface. However, the pp-HMDSO formed by low N₂O/HMDSO ratios tends to be physically soft, which will impose an integration issue when stacked with barrier layers. When an barrier layer stacks on top of soft pp-HMDSO layer, a wrinkled surface is formed and the soft pp-HMDSO layer loses the optical transmittance, which will not be suitable as top emission OLED devices.

In order to harden the buffer layer 312, plasma curing of the buffer layer 312 is utilized. At process 210, the buffer layer 312 is cured to form a hardened buffer layer 313, as shown in FIG. 3C. The curing may be performed by a fluorine-containing plasma, such as nitrogen fluoride (NF₃), silicon fluoride (SiF₄), fluorine gas (F₂), and/or carbon tetrafluoride (CF₄). The fluorine-containing plasma curing of the buffer layer 312 may be performed at a temperature less than 100 degrees Celsius so that the buffer layer is hardened without permitting the oxygen to diffuse to the OLED device. In one embodiment, a NF₃ gas is flowed into the chamber having a flow rate of 1100 standard cubic centimeter per minute (sccm), while a plasma is generated at 1200 watts. The chamber pressure is below 500 mTorr and the distance between the substrate 300 and the showerhead 106 is about 1 m. The plasma curing is performed at about 80 degrees Celsius and the curing duration is about 2 minutes. The hardened buffer layer 313 can maintain the flexibility and optical transmittance as one or more barrier layers are subsequently deposited thereon.

The curing duration may depend on the thickness of the buffer layer 312. In general, for every micrometer of the buffer layer 312, one minute of curing is performed. In one embodiment, the buffer layer 312 is about 2 micrometers thick, and the curing time is about two minutes.

At process 212, a second barrier layer 314 is deposited on the substrate 100, covering the hardened buffer layer 313 and the first barrier layer 308 formed on the OLED structure 304, as shown in FIG. 3D. The second barrier layer 314 includes a first portion 314a deposited over the first portion 313a of the hardened buffer layer 313 and a second portion 314b deposited over the second portion 313b of the hardened buffer layer 313.

The second barrier layer 314 may be a dielectric layer similar to the first barrier layer 308. The second barrier layer 314 is a dielectric layer such as SiN, SiON, SiO₂, or other suitable dielectric layers. The second barrier layer 314 may be deposited by a suitable deposition technique, such as CVD, PVD, spin-coating, or other suitable technique.

The deposition of the barrier layers and the buffer layer, and the curing of the buffer layer as described herein may be performed in a single deposition chamber, such as the PECVD chamber 100. Purging of the process chamber may be performed between cycles to minimize the risk of contamination. In one embodiment, the first barrier layer is deposited. The chamber is then purged so the gases used for the deposition of the first barrier layer are not present in the

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chamber for the subsequent processes. Next the buffer layer is deposited. The chamber is then purged again so the gases used for the deposition of the buffer layer are not present in the chamber for the subsequent processes. Next the buffer layer is cured, followed by another purge of the chamber. Lastly the second barrier layer is deposited. The single chamber process may be advantageous in reducing cycle times as well as reducing the number of chambers (and equipment costs) of using a multiple chamber process.

In summary, an OLED device is formed with a buffer 10 layer sandwiched between two barrier layers. The buffer layer is cured with a fluorine-containing plasma at a temperature less than 100 degrees Celsius prior to the deposition of a barrier layer thereon.

While the foregoing is directed to embodiments of the 15 present disclosure, other and further embodiments of the disclosure may be devised without departing from the basic scope thereof, and the scope thereof is determined by the claims that follow.

The invention claimed is:

1. A method for forming an organic light emitting diode (OLED) device, comprising:

depositing a first barrier layer on a region of a substrate having an OLED structure disposed thereon;

depositing a buffer layer on the first barrier layer; curing the buffer layer with a fluorine-containing plasma

at a temperature less than about 100 degrees Celsius; and

depositing a second barrier layer on the cured buffer layer.

- **2**. The method of claim **1**, wherein the buffer layer 30 comprises plasma-polymerized hexamethyldisiloxane (pp-HMDSO).
- 3. The method of claim 2, wherein a curing time is related to a thickness of the buffer layer, wherein the relationship is 1 minute per micrometer.
- **4**. The method of claim **1**, wherein the fluorine-containing plasma comprises NF₃, SiF₄, F₂, CF₄, or any combination thereof
- **5**. The method of claim **1**, wherein the depositing the first and second barrier layers and the buffer layer and the curing 40 of the buffer layer are performed in a single process chamber.
- $\pmb{6}$. The method of claim $\pmb{5}$, wherein the single process chamber is a PECVD chamber.
 - 7. A method for forming an OLED device, comprising: depositing a first barrier layer on a region of a substrate having an OLED structure disposed thereon;

depositing a buffer layer on the first barrier layer;

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curing the buffer layer with a fluorine-containing plasma for about 2 minutes; and

depositing a second barrier layer on the cured buffer layer.

- **8**. The method of claim **7**, wherein the buffer layer comprises plasma-polymerized hexamethyldisiloxane (pp-HMDSO).
- **9**. The method of claim **8**, wherein the curing the buffer layer with a fluorine-containing plasma is at a temperature less than about 100 degrees Celsius.
- 10. The method of claim 7, wherein the fluorine-containing plasma comprises NF₃, SiF₄, F₂, CF₄, or any combination thereof.
- 11. The method of claim 7, wherein the depositing the first and second barrier layers and the buffer layer and the curing of the buffer layer are performed in a single process chamber
- 12. The method of claim 11, wherein the single process chamber is a PECVD chamber.
- **13**. A method for forming an OLED device, comprising: forming a contact layer over a substrate;

forming an OLED structure over the contact layer; depositing a first barrier layer over the OLED structure; depositing a buffer layer on the first barrier layer;

curing the buffer layer with a fluorine-containing plasma at a temperature less than about 100 degrees Celsius; and

depositing a second barrier layer on the cured buffer layer.

- **14**. The method of claim **13**, wherein the buffer layer comprises plasma-polymerized hexamethyldisiloxane (pp-HMDSO).
- **15**. The method of claim **14**, wherein a curing time is related to a thickness of the buffer layer, wherein the relationship is 1 minute per micrometer.
- **16**. The method of claim **13**, wherein the fluorine-containing plasma comprises NF₃, SiF₄, F₂, CF₄, or any combination thereof.
- 17. The method of claim 13, wherein the depositing the first and second barrier layers and the buffer layer and the curing of the buffer layer are performed in a single process chamber.
- **18**. The method of claim **17**, wherein the single process chamber is a PECVD chamber.
- 19. The method of claim 13, wherein the first barrier layer comprises SiN, SiON, ${\rm SiO_2}$, ${\rm Al_2O_3}$, or AlN.
- 20. The method of claim 19, wherein the second barrier layer comprises SiN, SiON or SiO₂.

* * * * *

UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO. : 9,431,631 B2

APPLICATION NO. : 14/179350

DATED : August 30, 2016

INVENTOR(S) : Jrjyan Jerry Chen et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the claims

In column 6, line 29, in claim 14, delete "plasma-polymerized" and insert -- plasma-polymerized --, therefor.

Signed and Sealed this Thirteenth Day of December, 2016

Michelle K. Lee

Michelle K. Lee

Director of the United States Patent and Trademark Office